Sir:

10/628913 10/628913

Transmitted herewith for filing is the Patent Application of:

Stor: YEEN TAT CHAN, KHENG CHOK TEE, YIANG AUN NGA, ZHAO LUN, WANG LING GOH, DIING SHENP ANG

METHOD OF FORMING A PARTIALLY DEPLETED SILICON ON INSULATOR (PDSOI) TRANSISTOR WITH A PAD

OTHER THAN A SMALL ENTITY

4		
,	LOCK BODY EXTENSION	

Enclosed	are:
х	$\underline{5}$ sheets of drawing(s) - formal.
x	An assignment of the invention to Chartered Semiconductor Manufacturing Ltd.
	An associate power of attorney Applicant claims small entity status
	Request & Certification under 35 USC 122(b)(2)(b)(i)

(Col. 2)

The filing fee has been calculated as shown below:

(Col. I)

		(Col. 1)	(COL 2)	OTHER THAN	A SMACE DIVITI					
FOR:		NO. FILED	•NO. EXTRA	RATE	FEE					
BASI	C FEE	$\geq \leq$			\$ 750.					
TOTA	L CLAIMS	44 -20=	24	x 18 =	\$ 432.					
INDE	P CLAIMS	4 -3=		x 84 =	\$ 84.					
				SUB TOTAL	\$ 1,266.					
				ASSIGNMENT	\$40,					
_			TOTAL	\$ 1,306.						
X	Please charge my Deposit Account No. 19-0033 in the amount of \$ 1,306. A duplicate copy of this sheet is enclosed.									
х	The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any									
overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.										
X Any additional filing fees required under 37 CFR §1.16.										
	X A	Any patent application	patent application processing fees under 37 CFR §1.17.							
	Respectfully submitted,									

EXPRESS MAIL CERTIFICATE

Express Mail No.EV313928154US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.

Date of deposit

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77/29/03